

Sheet 1 of 1

Form 1449*
INFORMATION
Docket Number: G&C 31006.2-US-U1
Application Number: 09/938,978
Applicant: Alan N. Willson, Jr. et al.
Filing Date: August 24, 2001
Group Art Unit: 2121

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\ <u>\Z</u>			U.S. PATENT DOCUMENTS								
EXAMINAL INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FIEING DATE IF					
in	4,746,899	05/24/88	Swanson et al.			`C					
ly	4,751,666	06/14/88	Gillingham		PCK	TRANSPORT					
W	5,953,241	09/14/99	Hansen et al.		10/	20 6					
gu	6,308,190 B1	10/23/01	Willson Jr. et al.			C OD					
			FOREIGN PATENTS			Chia					
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES 200					
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<u>Q</u>	Chevillat Band-Lin pp. 1909	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Chevillat, Pierre R. et al., "Optimum FIR Transmitter and Receiver Filters for Data Transmission over Band-Limited Channels," IEEE Transactions on Communications, Vol. Com-30, No. 8, August 1982, pp. 1909-1915. Kuo, Tzu-Chieh et al., "A Programmable Interpolation Filter for Digital Communications Applications,"									
en	Proceedi Montere Kuo, Tzu	Proceedings of the 1998 IEEE International Symposium on Circuits and Systems, May 31-June 3, 1998, Monterey, CA, IEEE, 1998, Vol. II, pp. II-98 – II-100. Kuo, Tzu-Chieh, "A Programmable Interpolation Filter for Digital Communication Applications,"									
eu.	xi, 1-51. Moreau o	Thesis, Master of Science in Electrical Engineering, University of California, Los Angeles, CA, 1996, pp. xi, 1-51. Moreau de Saint-Martin, Francois et al., "Design of Optimal Linear-Phase Transmitter and Receiver Filters for Digital Systems," IEEE, 1995, pp. 885-888.									
eu		Proakis, John G. et al., "Digital Signal Processing: Principles, Algorithms, and Applications," Macmillian Publishing Company, New York, pp. 485-502, undated.									
W	Samueli,	Samueli, Henry, "On the Design of Optimal Equiripple FIR Digital Filters for Data Transmission Applications," IEEE Transactions on Circuits and Systems, Vol. 35, No. 12, December 1998, pp. 1542-									
lu	Specifica 1567.	Samueli, Henry, "On the Design of FIR Digital Data Transmission Filters with Arbitrary Magnitude Specifications," IEEE Transactions on Circuits and Systems, Vol. 38, No. 12, December 1991, pp. 1563-1567.									
CM	Solid-Sta pp. 194-1	Thompson, Charles D. et al., "A Digitally-Corrected 20b Delta-Sigma Modulator," IEEE International Solid-State Circuits Conference, Session 11/Oversampled Data Conversion/Paper TP 11.5, IEEE 1994, pp. 194-195.									
QU	Vaidyana 118-134,		ulirate Systems and Filter Banks,	" Prentice Hall	, Englewood C	Lliffs, NJ 07631, pp.					
(M	Kuskie C Symposiu	Kuskie C. et al. (1995) "A Decimation Filter Architecture for GHz Delta-Sigma Modulators," IEEE Int'l Symposium on Circuits and Systems, pp. 953-956.									
(M			V 11.8 mW Baseband ADC with ruits Conference, pp. 133-136.	n 72 dB Dynam	ic Range for G	SM," 1999, IEEE,					

EXAMINER:	NAO	DATE CONSIDERED:	5/	26/	104					
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in										
conformance and not considered. Include copy of this form for next communication to the Applicant.										

*Substitute Disclosure Statement Form (PTO-1449)

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